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In February 2018, Hennessy was announced as the new Chairman of Alphabet Inc., Google 's parent company. On March 21, 2018, together with David Patterson, he was awarded the 2017 ACM A.M. Turing Award for the development of the reduced instruction set computer (RISC) architecture in the 1980s..

### **John L. Hennessy - Wikipedia**

Reading: Patterson & Hennessy - B1-6. Topics: ALU design: full adder, slt operation, HDL design, carry lookahead Lecture slides (PDF) Programs: 4-bit-adder.vl, more examples of Verilog programs, mips-alu.vl, ALU4-mixed.vl Lecture Notes. Implementation of a full adder: Carry out logic; Result logic: using 'and', 'or' and inverter and using xor-gate.

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